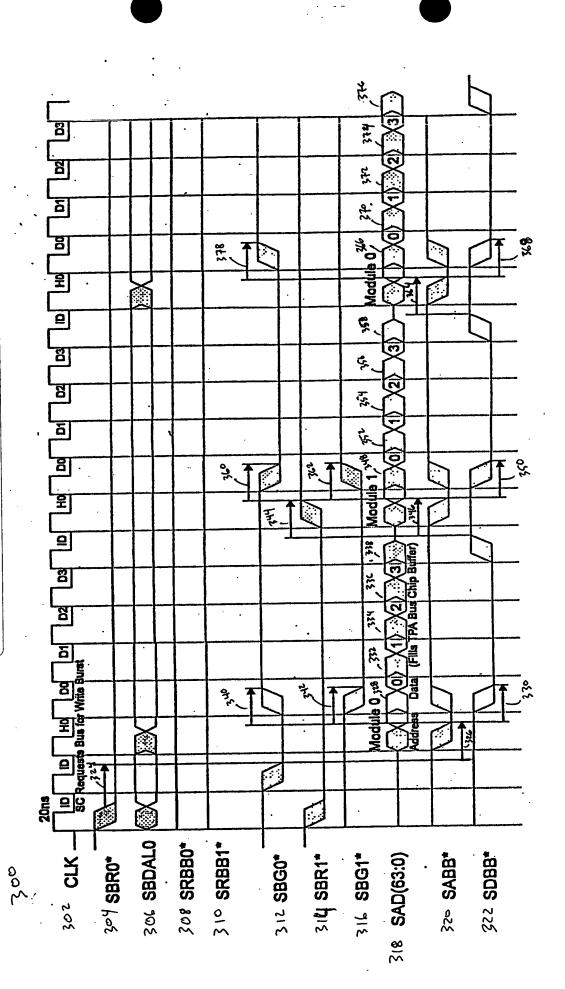
82

APPLN. FILING DATE: SEPTEMBER 20, 2001

IULTI-TIER SYSTEM BUS

TITLE: TWO LEV



SHEET 3 of 12

APPLICATION SERIAL NO: UNASSIGNED

INVENTOR(S): GREGORY S. ANDRE

APPLN. FILING DATE: SEPTEMBER 20, 2001 TITLE: TWO LEVEL MULTI-MIER: SASTEIM BUS

FIGURE 4

ह्व ह्व । प्व । क्व 154 425 Module 0 446 440 *\$* (a) (a) (a) (Fills †PA Bus Chip Buffer) S 2 435 82h SC Request 40C SBDALO 40% SRBBO* 410 SRBB1* 事2 CLK. 416 SBG1* 404 SBRO* 450 SABB* 412 SBG0* 422 SDBB* 414 SBR1* 418 SAD(63:0) 09 7

SHEET 4 of 12

APPLICATION SERIAL NO: UNASSIGNED

APPLN. FILING DATE: SEPTEMBER 20, 2001
TITLE: TWO LEVEL MULTI TIER SYSTEM BUS
INVENTOR(S): GREGORY S. ANDRE

r 20, 2001

TITLE: TWO LEVEL MULTI-TIER SYSTEM BUS

INVENTOR(S): GREGORY S. ANDRE

APPLICATION SERIAL NO: UNASSIGNED

SHEET 5 of 12

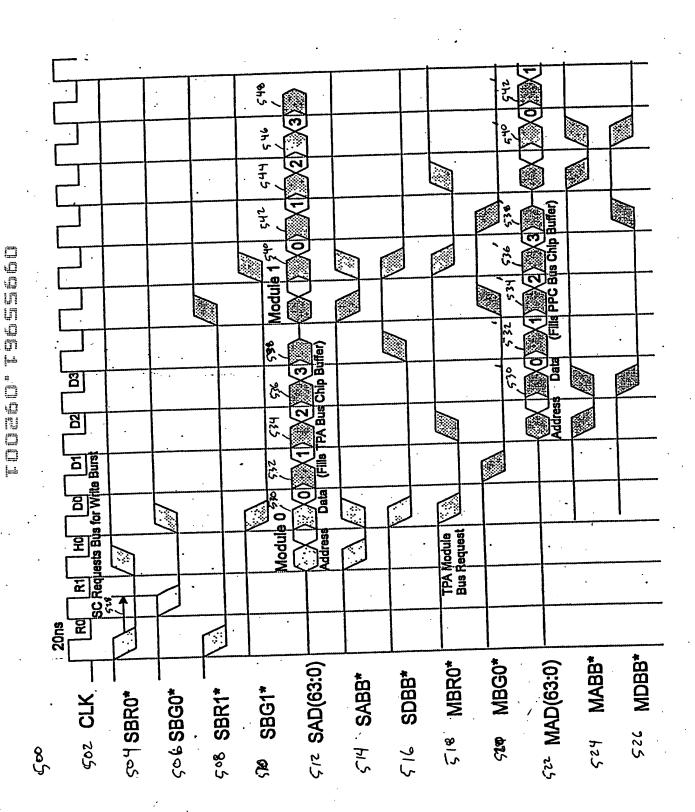


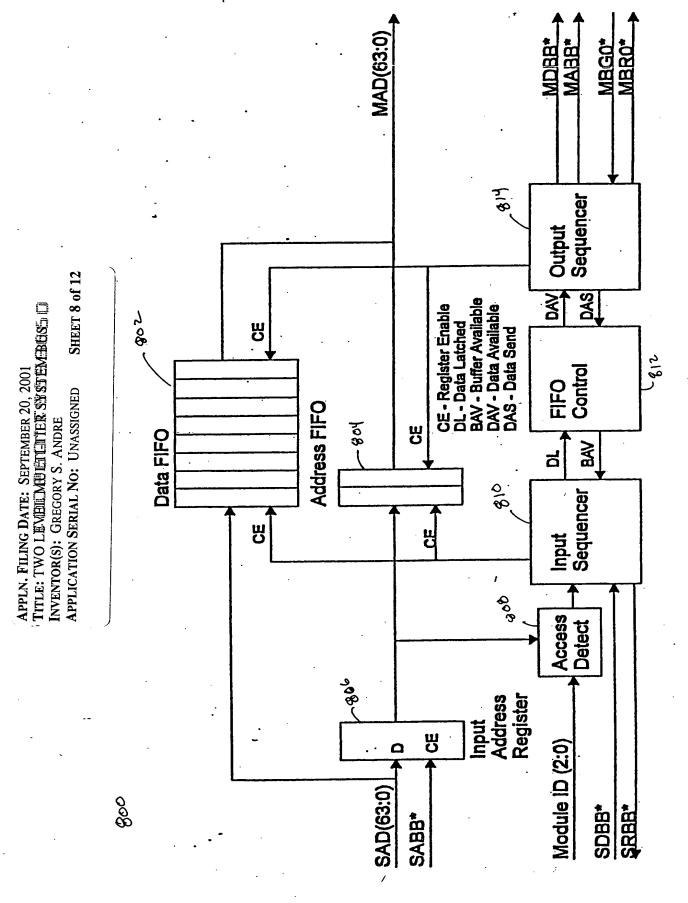
FIGURE 5

638 **5**5) SHEET 6 of 12 Module 6 (4) TITLE: TWO LEVEL MILL FI-TIER SYSTEM BUS INVENTOR(S): GREGORY S. ANDRE 634 ×638 . APPLICATION SERIAL NO: UNASSIGNED , g ដ 636 8 634 SC Requests Bus for Write Burst 632 ઠ્ઠ Module 0 TPA Module Bus Request Ro 829 MDBB* 620 MBG0* `...098S 7○°) 6.12 SAD(63:0) GIS MBRO* 622 MAD(63:0) 624 MABB* (16 SDBB* 614 SABB* 608 SBR1* 602 CLK 604 SBRO* 610 SBG1*

8

APPLN. FILING DATE: SEPTEMBER 20, 2001

160 438 252 754 SHEET 7 of 12 TITLE: TWO LENEL MULTITIERS ESTEM BUS (5) [] INVENTOR(S): GREGORY S. ANDRE 乳 46 APPLN. FILING DATE: SEPTEMBER 20, 2001 APPLICATION SERIAL NO: UNASSIGNED Modul (Fills TPA Bus Chip Buffer) 8 8 Data 734 SC Request 702 CLK 710 SBG1* 712 SAD(63:0) 716 SDBB* 714 SABB* Toll SBRO* 1058S. 90L 7०% SBR1*



F1602E 8

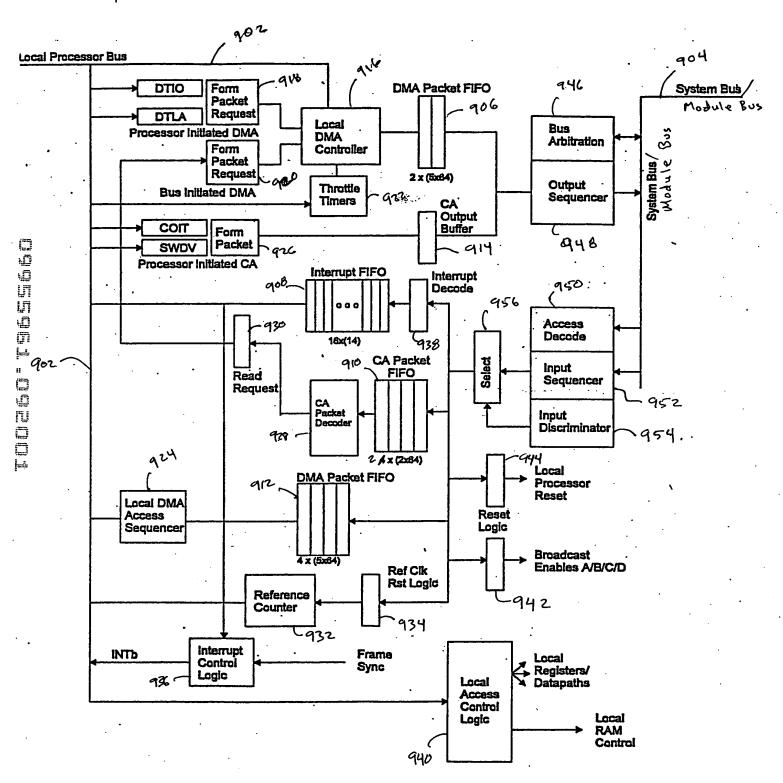
APPLN. SEPTEMBER 20, 2001

TITLE: The LEVEL MULTI-TIER SYSTEM BUS

INVENTOR(S): GREGORY S. ANDRE

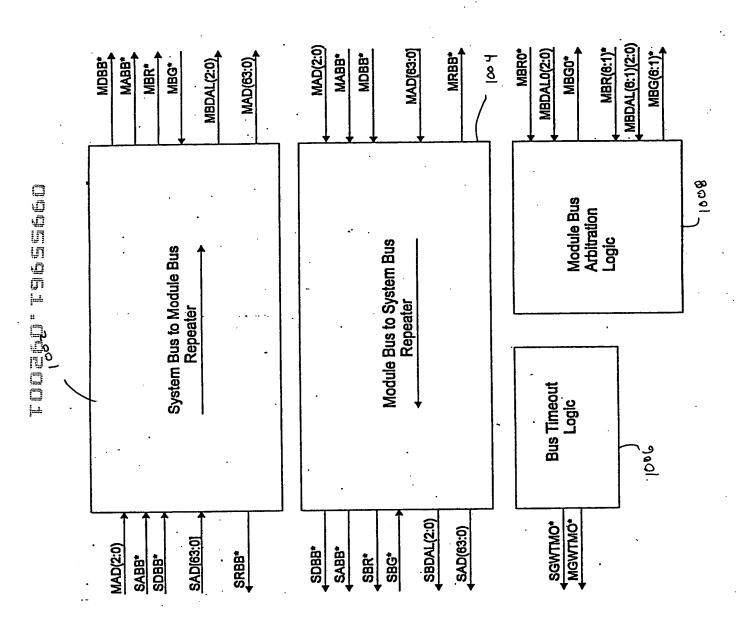
APPLICATION SERIAL NO: UNASSIGNED SHEET 9 of 12

700



APPLN. FIF DATE: SEPTEMBER 20, 2001
TITLE: TWEEVEL MULTI-TIER SYSTEM BUS
INVENTOR(S): GREGORY S. ANDRE
APPLICATION SERIAL NO: UNASSIGNED SHEET

SHEET 10 of 12.

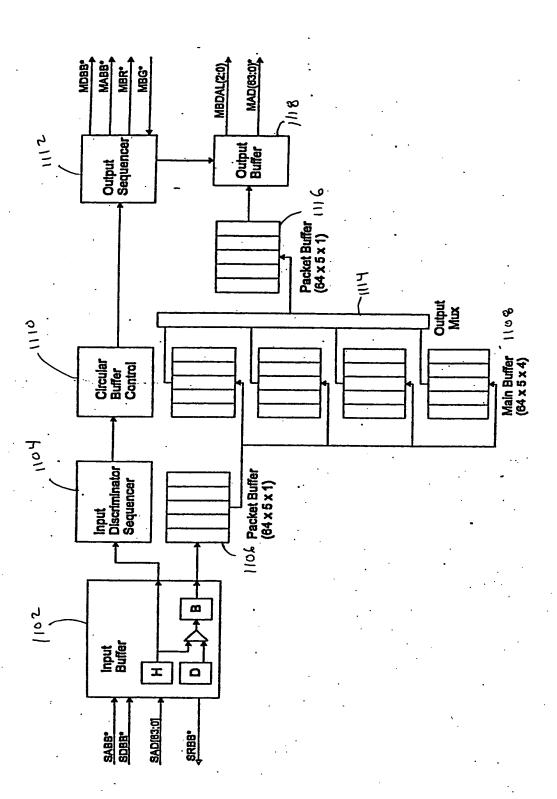


APPLN. FILE ATE: SEPTEMBER 20, 2001
TITLE: TWO LEVEL MULTI-TIER SYSTEM BUS

INVENTOR(S): GREGORY S. ANDRE
APPLICATION SERIAL NO: UNASSIGNED

SHEET 11 of 12

FIGURE

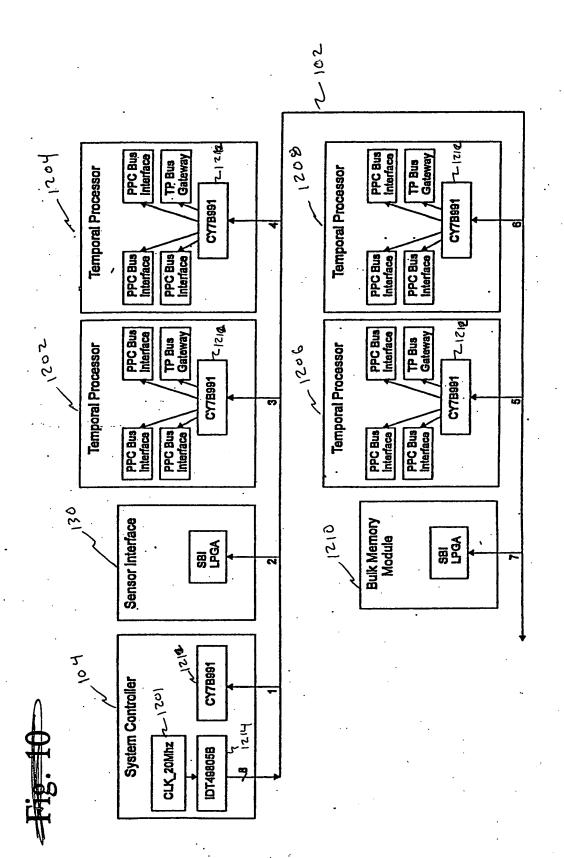


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INVENTOR(S): GREGORY S. ANDRE

SHEET 12 of 12 APPLICATION SERIAL NO: UNASSIGNED

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